IBM System x3850 M2 achieves leadership SPECint_rate2006 score for a 4-socket x86 system

x3850 M2 delivers top 4-socket x86 score for compute-intensive integer performance

September 16, 2008 ... The IBM® System x^{TM} 3850 M2 server, built with fourth-generation IBM Enterprise X-Architecture®, delivers innovation with enhanced reliability and availability features to enable optimal performance for databases, enterprise applications, and virtualized environments.

The x3850 M2 demonstrated leadership performance for a 4-socket x86 system on the SPECint_rate2006 member of the SPEC CPU2006 benchmark suite. SPECint_rate2006 measures the throughput, or rate, of a machine carrying out a number of tasks. Developed from actual end-user applications, the SPEC CPU2006 benchmark suite provides a comparative measure of integer and floating-point compute intensive performance for enterprises who run these kinds of workloads.

The x3850 M2 was configured with the Intel® Xeon® Processor X7460 (2.66GHz, 9MB L2 cache, and 16MB L3 cache—4 processors/24 cores/24 threads) and 64GB of memory, and SUSE Linux® Enterprise Server 10 x64 (SP2). (1)

The scores in the following table are the first SPEC CPU2006 results published for this x3850 M2 processor model.

SPEC CPU2006 Benchmark	x3850 M2 – Intel Xeon Processor X7460 (2.66GHz, 9MB L2 Cache, 16MB L3 Cache)
SPECint2006	25.5
SPECint_rate2006	294

Results are current as of September 16, 2008. The scores have been submitted to SPEC for review and will be posted on their Web site upon successful completion of the review. View all published results at www.spec.org.

(1) The x3850 M2 model using the Intel Xeon Processor X7460 is planned to be generally available October 10, 2008.

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